

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F17/50

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, COMPENDEX, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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X	ROBERTSON W ET AL: "RTL synthesis for systolic arrays" PROCEEDINGS OF THE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS. (ISCS). CHICAGO, MAY 3 - 6, 1993, NEW YORK, IEEE, US, vol. VOL. 2, 3 May 1993 (1993-05-03), pages 1670-1673, XP010115439 ISBN: 0-7803-1281-3 the whole document ----- -/--	1-22
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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
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Date of the actual completion of the international search

20 September 2004

Date of mailing of the international search report

04/10/2004

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>POTKONJAK M ET AL: "Cost optimization in ASIC implementation of periodic hard-real time systems using behavioral synthesis techniques"</p> <p>1995 IEEE/ACM INTERNATIONAL CONFERENCE ON COMPUTER-AIDED DESIGN (ICCAD). SAN JOSE, NOV. 5 - 9, 1995, IEEE/ACM INTERNATIONAL CONFERENCE ON COMPUTER-AIDED DESIGN, LOS ALAMITOS, IEEE COMP. SOC. PRESS, US, 5 November 1995 (1995-11-05), pages 446-451, XP010153094</p> <p>ISBN: 0-8186-7213-7</p> <p>the whole document</p>	1
X	<p>-----</p> <p>SHENOY U N ET AL: "A System-level Synthesis Algorithm with Guaranteed Solution Quality"</p> <p>PROCEEDINGS OF THE CONFERENCE ON DESIGN, AUTOMATION AND TEST IN EUROPE, 27 March 2000 (2000-03-27), pages 417-424, XP010377500</p> <p>PARIS, FRANCE</p> <p>the whole document</p>	1
A	<p>-----</p> <p>US 5 367 473 A (CHU LYNN H ET AL)</p> <p>22 November 1994 (1994-11-22)</p>	
A	<p>-----</p> <p>US 4 888 692 A (GUPTA SUBHASH ET AL)</p> <p>19 December 1989 (1989-12-19)</p> <p>-----</p>	

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5367473	A	22-11-1994	NONE	
US 4888692	A	19-12-1989	US 5260868 A	09-11-1993